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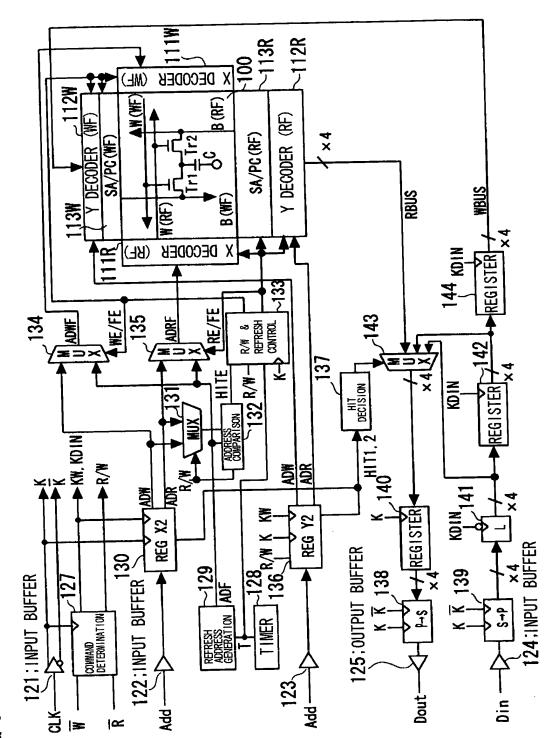
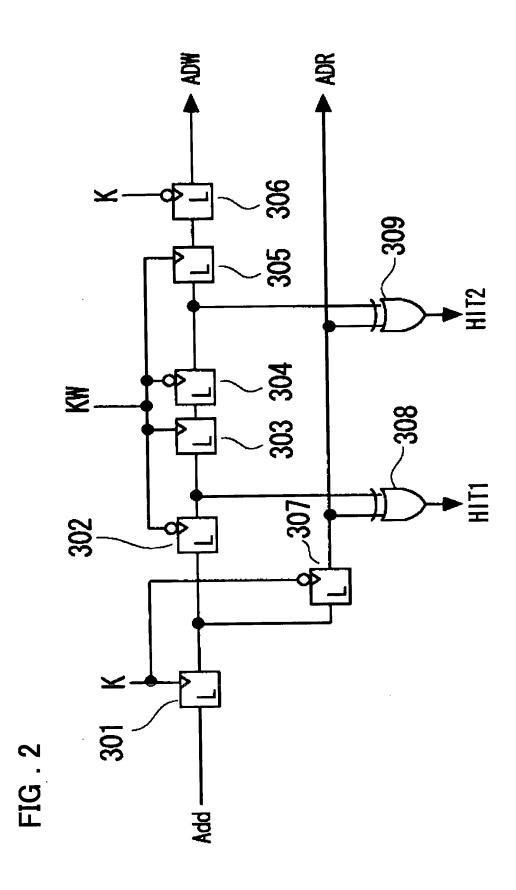


FIG.

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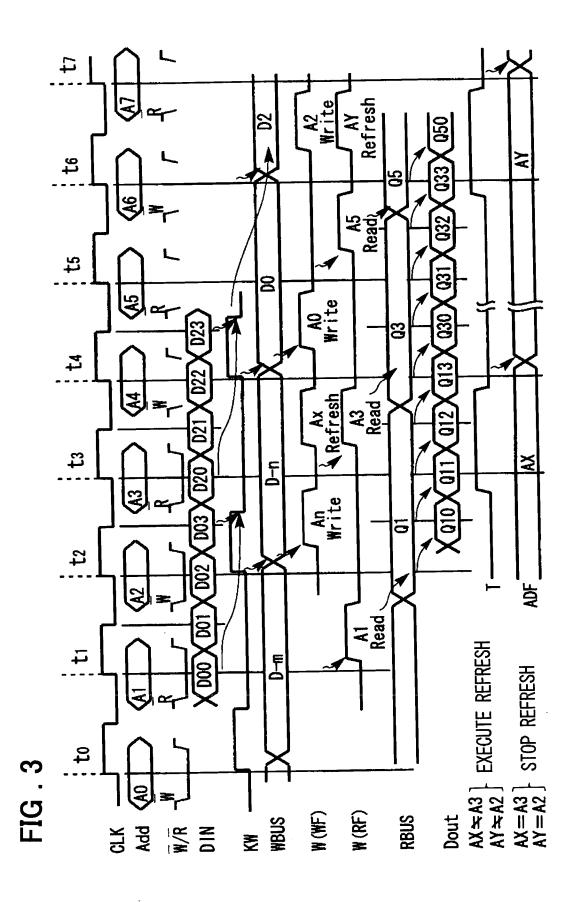
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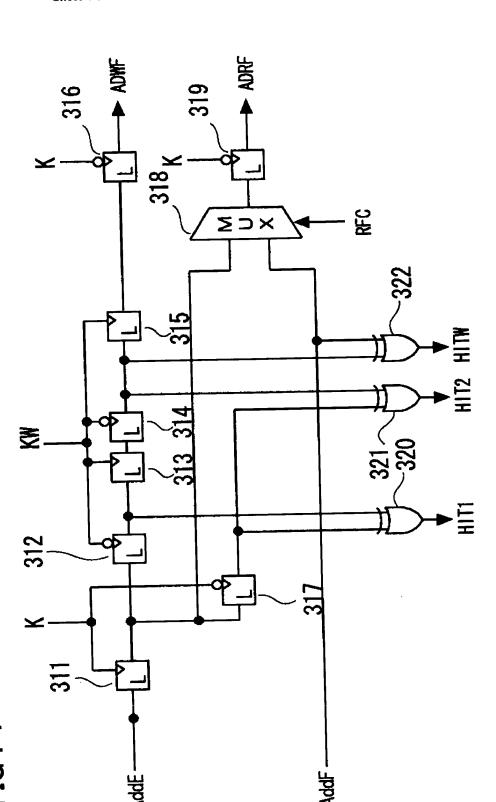
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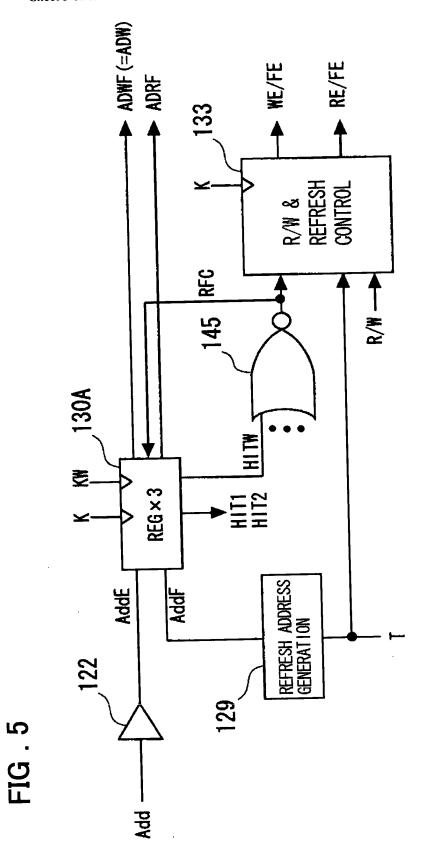
Title: SEMICONDUCTOR MEMORY DEVICE AND CONTROL METHOD THEREOF

Inventor: Hiroyuki Takahashi Filing Date: October 23, 2003 Serial No.: To be assigned

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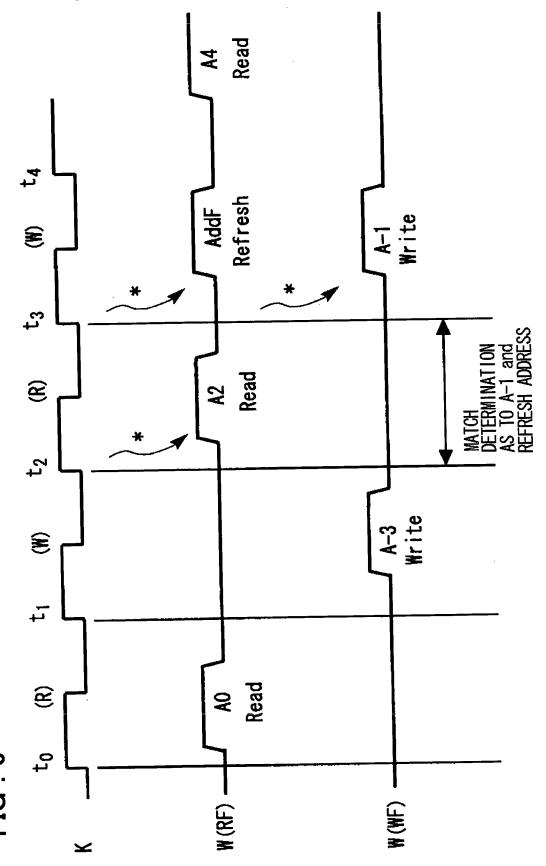


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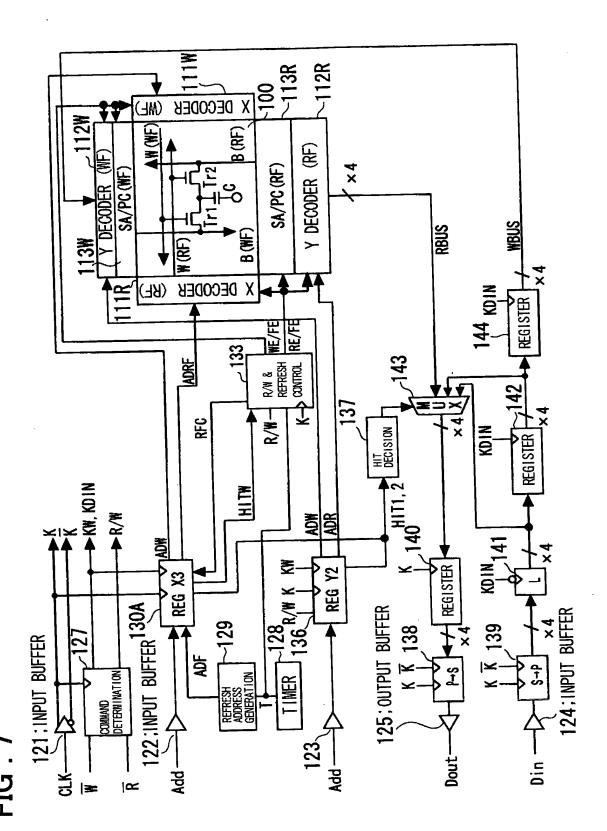
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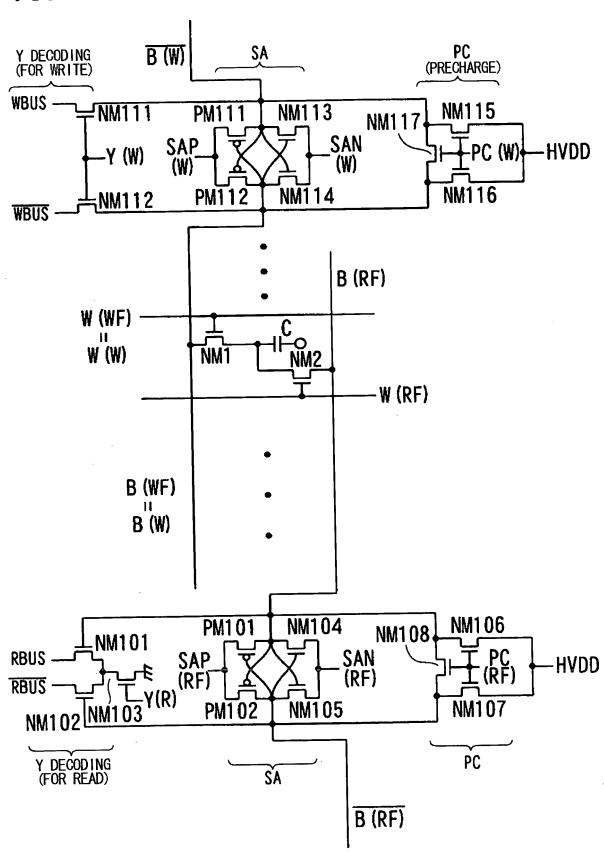


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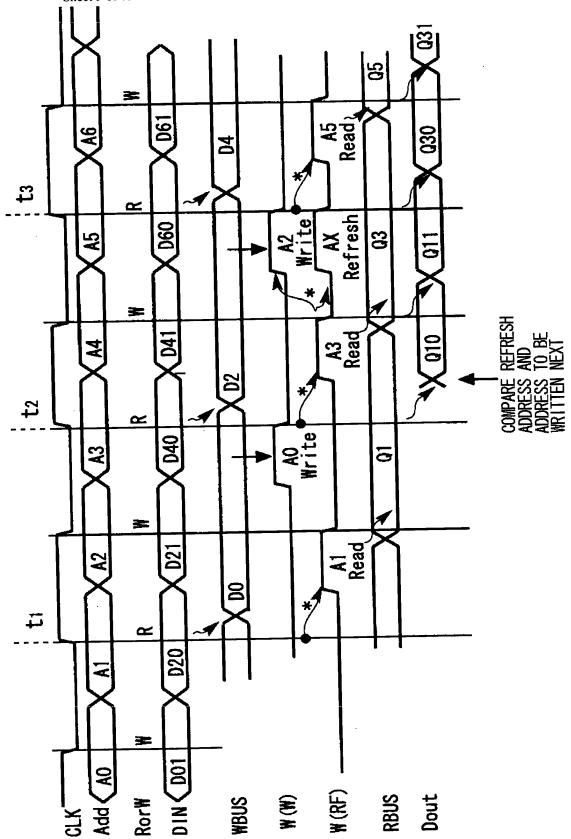
FIG.8



CONTROL METHOD THEREOF

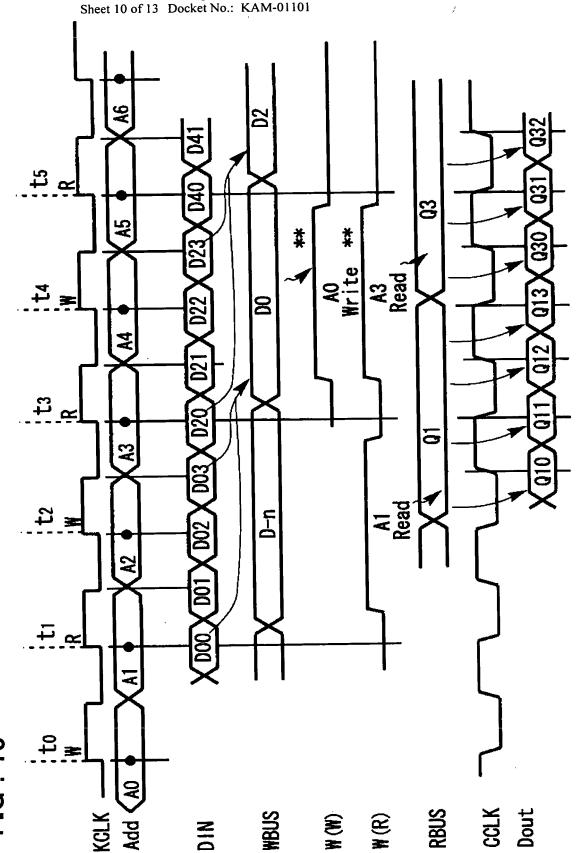
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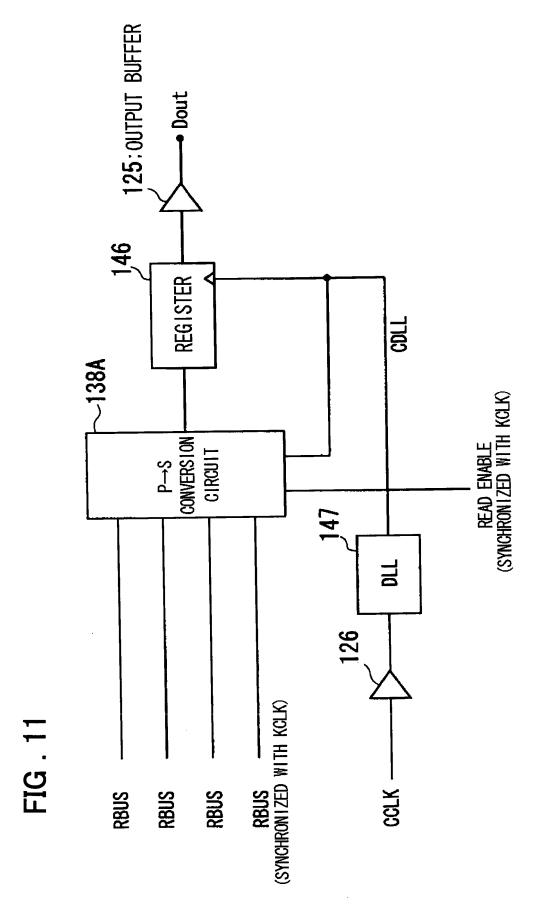
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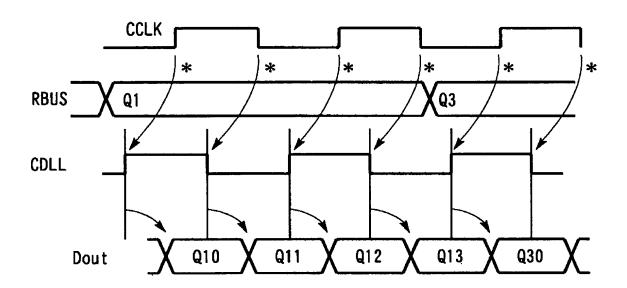


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FIG . 12



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FIG . 13 PRIOR ART

